

AMENDMENTS TO THE CLAIMS

Listing of Claims:

Claims 1-3 (Canceled)

4. (Currently Amended) [[An]] A multimode electronic image frame, comprising:
a tuning unit [[for]] receiving a television signal and dividing said television signal into a first analog video signal and a first audio signal;
a multimedia player processing unit [[for]] decoding multimedia files into a second analog video signal and a second audio signal;
a video processing unit [[for]] converting said first and second analog video signals into a digital video signal;
a display unit [[for]] displaying said digital video signal;
an audio processing unit [[for]] amplifying said first and second audio signals; and
a microprocessor coupled to said tuning unit, said multimedia player processing unit, said video processing unit, said display unit and said audio processing unit, and [[for]] controlling operations thereof with control signals;
wherein, the electronic image frame displays digital pictures in a digital picture frame mode and displays television programming in a television mode.

5. (Currently Amended) The electronic image frame according to claim 4, said multimedia player processing unit comprising:
a memory card driving unit [[for]] transmitting said multimedia files to said multimedia processing unit;
a multimedia processing unit [[for]] decoding multimedia files into said second analog video signal and said second audio signal;
an audio outputting unit [[for]] outputting said second audio signal;
a video outputting unit [[for]] outputting said second video signal; and
a control processing unit coupled to said microprocessor and [[for]] controlling said multimedia processing unit in response to said control signals.

6. (Currently Amended) The electronic image frame according to claim 5, said multimedia player processing unit further comprising:

a flash storage [[for]] storing program executed by said control processing unit;

a buffer storage [[for]] temporarily storing data generated during operation of said multimedia player processing unit; and

an electrically erasable memory for storing parameters for operation of said multimedia player processing unit.

7. (Currently Amended) The electronic image frame according to claim 4, said video processing unit comprising an A/D conversion unit [[for]] converting said analog video signal into digital signal, a video signal decoding unit [[for]] decoding said digital signal, and a signal processing unit [[for]] transforming said decoded digital signal into digital video signal and sending digital video signal to said display unit.

8. (Previously Presented) The electronic image frame according to claim 4, wherein said microprocessor is coupled to said tuning unit, said multimedia player processing unit, said video processing unit, said display unit and said audio processing unit via I²C serial bus.

9. (Currently Amended) The electronic image frame according to claim 4, further comprising an infrared control unit coupled to said microprocessor, [[for]] receiving an IR controlling signal, converting said IR controlling signal into a digital serial signal and sending said digital serial signal to said microprocessor.